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6/10/02

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Swee Yew Choe
Assignee: Sun Microsystems, Inc.
Title: METHOD FOR CLOCK CONTROL OF HALF-RAIL
DIFFERENTIAL LOGIC
Serial No.: 09/927,566 Filed: August 9, 2001
Examiner: D. Chang Group Art Unit: 2819
Docket No.: P-6467

Monterey, CA
May 31, 2002

Assistant Commissioner for Patents
Washington, D.C. 20231

AMENDMENT

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TECHNOLOGY CENTER 2880

Dear Sir:

In response to the Office Action dated April 1, 2002,
enclosed herewith are:

1. A separate paper entitled "Clean Copy of Replacement Claims" with a clean version of the claims (8 pages); and
2. A separate paper entitled "Version with Markings to Show Changes Made" with markings to show amendments to the claims (8 pages).

The above papers are incorporated herein by reference in their entireties as a part of this paper.